

FRONT-CARD DIP SWITCH:

The V.35 Sync Modem Eliminator Card has one four-position DIP switch mounted on the daughterboard. The figure on the first page shows the numbering of the individual switches and their ON/OFF positions relative to the daughterboard.

S1 through S3: Data Rate Settings are set in combination to determine the synchronous data rate for the V.35 Sync Modem Eliminator Card. Switch S4 is not active.

<u>S1:</u>	<u>S2:</u>	<u>S3:</u>	<u>S4:</u>	SETTING:
OFF	OFF	OFF	N/A	32 Kbps
ON	OFF	OFF	N/A	48 Kbps
OFF	ON	OFF	N/A	56 Kbps
ON	ON	OFF	N/A	64 Kbps
OFF	OFF	ON	N/A	72 Kbps
ON	OFF	ON	N/A	112 Kbps
OFF	ON	ON	N/A	128 Kbps
ON	ON	ON	N/A	144 Kbps

FRONT-CARD JUMPERS:

The V.35 Sync Modem Eliminator Card has two ports (Port A and Port B), and each must be configured independently. Therefore, every function (such as clocking) has two jumpers associated with it. The figure on the first page shows each jumper has three possible positions: strap covering posts 1 and 2, strap covering posts 2 and 3, or removing the strap altogether. The V.35 Sync Modem Eliminator Card has six jumpers (JP1 through JP6) mounted on the PC board. These jumpers set clocking, carrier, and RTS/CTS delay independently for ports A and B.

JP1: RTS/CTS Delay (Port A) determines with respect to Port A-- the amount of delay between the time the V.35 Card "sees" RTS and when it sends CTS. In order to emulate either dialup or leased-line modems, you can set this strap at either 0, 7, or 53 ms.

JP1:Setting:Position 1 and 27-ms delay (factory default)Position 2 and 353-ms delayStrap Removed0-ms delay

JP2: Carrier Control (Port A) determines with respect to Port A--whether the V.35 Card's carrier is constantly on or controlled by RTS. JP2: Setting:

` <u>J́P2:</u>
Position 1 and 2
Position 2 and 3
Strap Removed

Carrier Constantly ON (factory default) Carrier Controlled by RTS Not a valid setting

JP3: Carrier Control (Port B) determines with respect to Port B--whether the V.35 Card's carrier is constantly on or controlled by RTS.

JP3: Position 1 and 2 Position 2 and 3 Strap Removed

Carrier Constantly ON (factory default) Carrier Controlled by RTS Not a valid setting

JP4: Clock Source (Port B) determines with respect to Port B--whether clocking is internal or receive loopback.

Settina:

NOTE:

If Port B is set to receive loopback clock, Port A *must* be set for external clock. Port B may *not* be set for external clock.

JP4: Position 1 and 2 Position 2 and 3 Strap Removed Setting: Received Loopback Clock Internal Clock (factory default) Not a valid setting

JP5: Clock Source (Port A) determines with respect to Port A--whether clocking is internal or external.

NOTE:

Only Port A may be set for external clock.

<u>JP5:</u>
Position 1 and 2
Position 2 and 3
Strap Removed

Setting: External Clock Internal Clock (factory default) Not a valid setting

NOTE:

DSR will raise upon the activating of DTR from remote.

SPECIFICATIONS:

Transmission Format: Synchronous, CCITT V.35.

Data Rates: 32, 48, 56, 64, 72, 112, 128, and 144 Kbps.

Clocking: Internal, External, or Receive loopback.

External Interface: UD-26 female, ports A1 and B1.

Internal Interface: Connection to MicroRack 2, 4, 8, or 16 chassis via male card edge.

Distance: 300 feet (91.4 m), DTE to DTE.

Indicators: Independent bicolor LED indicators for each port: TD, CD, and DTR; common "Power" indicator.

Fuse: 400 mA for 120V applications; 200 mA for 240V applications.

Power Supply: PS460A: Rackmount power supply is switchable between 120 and 240 VAC; Chassis supplies 10 VAC to the Card, typical consumption is 700 mW/2.4 amp. PS461A: 48-VDC rackmount power supply, typical consumption is 700 mW/2.4 amp.

RTS/CTS Delay: Strap-selectable for 0, 7, or 53 ms (+/- 15%)

JP6: RTS/CTS Delay (Port B) determines with respect to Port B -- the amount of delay between the time the V.35 card "sees" RTS and when it sends CTS. In order to emulate either dialup or leased-line moderns, you can get this strap at either 0, 7, or 53 ms.

> JP6: 7-ms delay (factory default) Position 1 and 2 53-ms delay Position 2 and 3 Strap Removed

REAR-CARD CONFIGURATION:

The rear interface card for the V.35 Card is equipped with two female UD-26 connectors, one for each DTE host port. This card has one configuration jumper (JB4). The figure on the first page shows the location of this jumper on the PC board.

SGND & FRGND (JB4) in the connected position, this jumper links UD-26 pin 7 (Signal Ground) and Frame ground. In the open position, Pin 1 is "lifted" from frame ground.

JB4: Setting: Position 1 and 2 = SGND (UD-26 pin 7) and FRGND Connected Position 2 and 3 = SGND (UD-26 pin 7) and FRGND Not Connected

Setting:

0-ms delay